

Mamillu Yaragani

Department: Electronics and Communication Engineering

College: Seshadri Rao Gudlavalleru Engineering College

Mobile: 9502815257

E-Mail: mamillu.yaragani4a6@gmail.com



RESIDENTIAL ADDRESS:

Y.Mamillu,

S/o: Y. Venkateswara Rao, Dno:2-2, Vemavaram village, Gudlavalleru mandal, Krishna district, AP-521331

ACADEMIC QUALIFICATIONS:

Ph.D : Ph.D Scholler(JNTUK)

M.Tech(ES) : Gudlavalleru Engineering College, Gudlavalleru in 2018

B.Tech (ECE) : SVIET, Nandamuru in 2015

Intermediate (MPC) : Viadyalaya JR College, Gudivada in 2011

S.S.C : ZPHS, Vadlamannadu in 2009

ACADEMIC REWARDS / ACHIEVEMENTS:

- Secured III rank in M.Tech I sem
- Secured II rank in M.Tech II sem

PROFESSIONAL EXPERIENCE: 6.3 years

Designation	Institution	Period
Asst. Professor	Gudlavalleru Engineering College	22-10-2018 to Till date

PROJECTS GUIDED:

PHD	Post Graduation	Graduation
Nil	Nil	3

SPECIALIZATION: Embedded systems

RECENT PUBLICATIONS/CONFERENCES:

International/ National Journals	International/ National Conferences
5	2

Journals Papers:

[1] Y Mamillu, Dr. T Ananda Babu, Dr. S. Ravi, Dr. P Rajesh Kumar “Analysis of Mental Stress during Public Speaking using Feature Optimization Techniques on EEG Signals” in Book Human-Machine Interface Technology Advancements and Applications published by CRC Press of Taylor & Francis, 2023. ISBN 9781032351520. **(Book Chapter)**

[2] Y.Mamillu, Vankayalapati Pavan Srikar, " A Low Power Radix 4 Adder Using Domino Logic" in IOSR Journal of Electronics and Communication Engineering, ISSN 2278-2834, Volume- 18, Issue-2, PP-1-4, april-2023, DOI : 10.9790/2834-1802011015

[3] Y.Mamillu, Dr. T. Ananda Babu, K. V. Venkateswara Rao " Classification of Mental Stress during Public Speaking using Electroencephalography Signals" in Journal of Huazhong University of Science and Technology, ISSN -1671-4512, Volume-50, Issue-7, PP-1-4, July-2021.

[4] Y.Mamillu, Shabana, Haritha Sri, Fayaz " Efficient sequence detector using reversible logic" in Journal Interdisciplinary cycle Reserch, ISSN -0022-1945, Volume-XIV, Issue-IV, PP-1204-1211, April-2022.

[5] Y.Mamillu, Y. Sri Chakrapani, Dr. M. Kamaraju "FPGA Implementation of Integral Image generator in SURF detector" in International Journal of Electronics Engineering(IJEE), Volume-10, Issue-2, PP-233-240, June-2018.

Conference Papers:

[1] “ Analysis of Mental Task ability in Students based on Electroencephalography signals”, IEEE International Conference on Signal Processing, Informatics, Communications and energy Systems(SPICES 2022), MBCET, Trivandrum, March 2022.

[2] "FPGA Implementation of Integral Image generator in SURF detector" in the Online National conference on Communications and Computing (NC3-2020), organized by Department of Electronics and Communication Engineering, Bapatla Engineering College, Bapatla, Guntur (Dt), Andhra Pradesh, INDIA, During 28th- 29th July 2020.

LIST OF WORKSHOPS/FDP’S / WEBINARS /SEMINARS ATTENDED: 26

➤ Participated in Online Faculty Development Training Program on “IOT Embedde Systems”

organized by department of ECE, PVP Siddhartha Institute of Technology, Kanuru, during 23rd – 27th Nov 2024

- Participated in AICTE Online Faculty Development Training Program on “Semiconductor Devices, Modelling & Circuit Technology” organized by department of ECE, Lendi Institute of engineering and Technology, during 19th– 24th Aug 2024
- Participated in Faculty development Program on “Applications of Computer Vision using Deep Learning” organized by department of ECE, Seshadri Rao Gudlavalleru Engineering college, during 20th – 25th Feb 2023
- Participated in a webinar on “Indians 5G Future” organized by IFREPO on 15th October 2022.
- Participated in Faculty Training Program on “Full Custom IC Design Using CADENCE Software Tools” organized by department of ECE, Seshadri Rao Gudlavalleru Engineering college, during 04th – 05th Aug 2022
- Participated in AICTE-ISTE Online Training Program on “Millimeter wave Frequencies(Phase-III)” organized by department of ECE, Gudlavalleru Engineering college, during 17th – 24th Aug 2021
- Participated in AICTE Online Short Term Training Program on “FPGA based Digital system design with HDL” organized by department of ECE, Dr. Rangarajan Dr. Sakunthala Engineering college, during 28th – 03th July 2021
- Participated in ATAL FDP on “Biomedical Instrumentation & IoT” organized by department of ECE, Sri Krishna College of Technology, during 21th – 29th Sep 2020
- Participated in AICTE Online Short Term Training Program on “Internet of Things (Everything) –Future” organized by department of CSE, B S Abdur Rahman Crescent Institute of Science and Technology, during 24th – 29th August 2020.
- Participated in AICTE Online Short Term Training Program on “Blockchain Architecture Design and Use Cases (Smart Contracts with Ethereum Solidity) Phase-III” organized by department of ECE, GEC, Gudlavalleru during 24th – 29th August 2020.
- Participated in AICTE Online Short Term Training Program on “Blockchain Architecture Design and Use Cases" (Decentralized Network Synchronization) Phase-II” organized by department of ECE, GEC, Gudlavalleru during 17th – 22th August 2020.
- Participated in AICTE Online Short Term Training Program on “Blockchain Architecture Design and Use Cases (Phase-I)” organized by department of ECE, GEC, Gudlavalleru during 10th – 15th August 2020.
- Participated in a Webinar on “Intellectual Property Rights & Patents - A View” organized by IQAC, GEC, Gudlavalleru on 9th June, 2020.
- Participated in online faculty development program on “Cyber Security” organized by

department of CSE, GEC, Gudlavalleru during 22th – 26th July 2020.

- Participated in one week Refresher course on “Embedded System Design” organized by department of ECE, GEC, Gudlavalleru during 27th – 01th august 2020.
- Participated in online faculty development program on “Image Processing and Computer vision-Industrial Scope” organized by department of ECE, MLWEC, Guntur during 8th – 12th July 2020.
- Participated in a Webinar on “Impact of COVID in Engineering Education & Way forward” organized by ISTE, GEC, Gudlavalleru on 9th June, 2020.
- Participated in one week International online Knowledge Development Program on “Challenges and Advancements in the Design of IOT, Embedded and VLSI Systems: A Researchers View” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 8th – 13th June 2020.
- Participated in online faculty development program on “Artificial Intelligence and its Applications” organized by department of CSE, Ramachandra College of Engineering, Eluru during 25th – 30th may 2020.
- Participated in online faculty development program on “Artificial Intelligence” organized by department of CSE, Santhiram Engineering College, Hyderabad during 13th – 18th may 2020.
- Participated in a Webinar on “Radio Frequency Measurements for Cellular and Wireless Communication Systems” organized by Department of Electronics and Communication Engineering, GEC, Gudlavalleru on 25th May, 2020.
- Participated in online FDP on “Digital Image Processing using MATLAB” hosted on Skiltohire from 24th-27th April 2020
- Participated in one and half week online faculty development program on “Introduction to Internet of Things” organized by NPTEL-AICTE during January to-march 2019.
- Participated in one week online faculty development program on “Embedded System Design with ARM” organized by NPTEL-AICTE during January to-march 2019.
- Participated in 2-day training program on “Design and Development of Digital circuits using VLSI Tools” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 6th – 7th Dec 2018.
- Participated in faculty development program on “Signal and image processing using LabVIEW” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 13th – 18th nov 2017.
- Participated in 2-day Training Program on “Mentor Graphics Tools” organized by department

of ECE, Gudlavalleru Engineering College, Gudlavalleru during 31st aug – 1st sep 2018.

- Participated in 2-day faculty training program on “Advances in VLSI Design and Embedded Systems” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 6th – 7th nov 2017.

NPTEL Certifications: 3

- Introduction to Internet of things with 79% (Silver).
- Embedded System Design with ARM with 80% (Silver).
- Hardware Modelling Using Verilog with 70%(Elite)
- Introduction to Programming in C with 60%(Elite).

MEMBERSHIP IN PROFESSIONAL BODIES:

S.No	Name of Professional Body	Membership ID
1	IAENG	293085
2	IFREP	PMIN78214639

Declaration:

I here declare that all the above-furnished information is correct to the best of my Knowledge.

Date:

Place: Gudlavalleru

Signature

(Y.Mamillu)